

## Description

The digital IP researcher will work in the R&D digital team and will contribute to the development of innovative IP for secure products. Main activities include various technical aspects as target specifications, technology survey, concept development and validation, prototyping, IP packaging, documentation, etc.

He could be also visible from customers side in relationship with Invia marketing through market needs enquiries, technical meeting, deliveries to the customer, field support, etc.

## Qualifications

PhD Degree in digital IC design, MSc Degree in electrical engineering or equivalent.

Applying for a first post-PhD job is a plus.

Both beginners and experienced designers are welcome.

## Skills

The applicant skills should include:

- experience with RTL development (VHDL or Verilog);
- experience with Design Verification and Validation;
- knowledge in the design of most of the following blocks: SOC, microcontroller architectures, communication protocols, cryptographic algorithms, secure platforms...;
- experience with design CAD flows (Cadence/Synopsys/Mentor);
- ability to develop emulators and test fixture hardware, PCB and FPGA prototypes;
- ability to troubleshoot electronic circuitry;
- knowledge in C language: compiler, linkers, DLL and embedded softwares;
- ability to write technical documents in English;
- creativity.

Knowledge in security, smartcard and cryptography are a plus.

## Applying

Please send your cover letter and resume to [job@invia.fr](mailto:job@invia.fr)